

Report Period: Sept 6 - 20

Summary of Progress in this Period

- Determined ADC to be used - SAR
 - Download and learn TensorFlow
 - Create a demo in TensorFlow
 - Gather Machine Learning Set
 - Create and host website
 - Researched individual components needed for stethoscope circuit
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Pending Issues

- Complete Section 1 of design document
 - Create Hardware tasks
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Plans for Upcoming Reporting Period

- Complete Design Doc Section 2
 - Create concrete implementation plans for ADC
 - Create Prototype of lower band pass filter / Experiment in Simulink
 - Add back-end storage to website
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